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Hiroshi Saitou

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03/11/2009

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EXAMINER

KHAN, ASHER R

ART UNIT

PAPER NUMBER

2621

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PAPER

**Please find below and/or attached an Office communication concerning this application or proceeding.**

The time period for reply, if any, is set in the attached communication.

## DETAILED ACTION

### *Response to Arguments*

Applicant's arguments filed 2/17/2009 have been fully considered but they are not persuasive.

In re page 2 line 15 – 28, Applicants argue that Katayama does not disclose or suggest a file management portion that sets a data recording unit of data that is to be supplied to the semiconductor memory pack device to a common multiple of a size obtained by adding up the sizes of erase blocks of the plurality of flash memories and a data management size of the file management portion. Specifically applicants argue that Katayama does not take into account the data management size of the file management portion.

In response the Examiner respectfully disagrees. Katayama discloses a file management portion (Fig. 1, file control circuit, 4) sets a data recording unit of data (**1024 or 2048 bytes of data**; Col. 4, lines 53-67; Col. 5, lines 1-13) and that is to be supplied to the semiconductor memory pack device to a common multiple ( **1024 or 2048 bytes**, Col. 4, lines 53-67; Col. 5, lines 1-13) of a size obtained by adding up the sizes of erase blocks (512 times 4=2048) of the plurality of flash memories and a data management size ( **512 times 2 = 1024 or 512 times 4= 2048**) of the file management portion. Katayama discloses a data management size ( **512bytes times 2 =1024 bytes , Fig. 11 or 512 bytes time 4 = 2048 bytes or Fig. 10, 221**) (Four chip parallel access operation Col. 5, line 15; Fig. 10) of the file management portion.

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In re page 3 line 1-11, Applicants argue that the size of the data recording unit of Katayama can never be set to a common multiple of the size obtained by adding up the sizes of erase blocks of plurality of flash memories of Katayama.

In response the Examiner respectfully disagrees. Katayama discloses the size of the data recording unit ( **512 +512= 1024 bytes; Col. 2, lines 22-23; 512 bytes is a unit erasure block;Fig. 11**) of Katayama can be set to a common multiple (**1024 bytes Fig. 11, 221**) of the size obtained by adding up the sizes of erase blocks (**adding of 512+512 =1024 bytes; Fig. 11 201, 202**) of plurality of flash memories of Katayama. Since the 512bytes (size of one erasure block) + 512bytes (size of another erasure block) = 1024 bytes (a common multiple) (Fig. 11). A data recording unit of 1024 bytes can be set to a common multiple of 1024 bytes.

### ***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to ASHER KHAN whose telephone number is (571)270-5203. The examiner can normally be reached on 9:00 AM to 5:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Marsha Banks- Harold can be reached on (571)272-7905. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Art Unit: 2621

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/A. K./

Examiner, Art Unit 2621